

Fig.1.

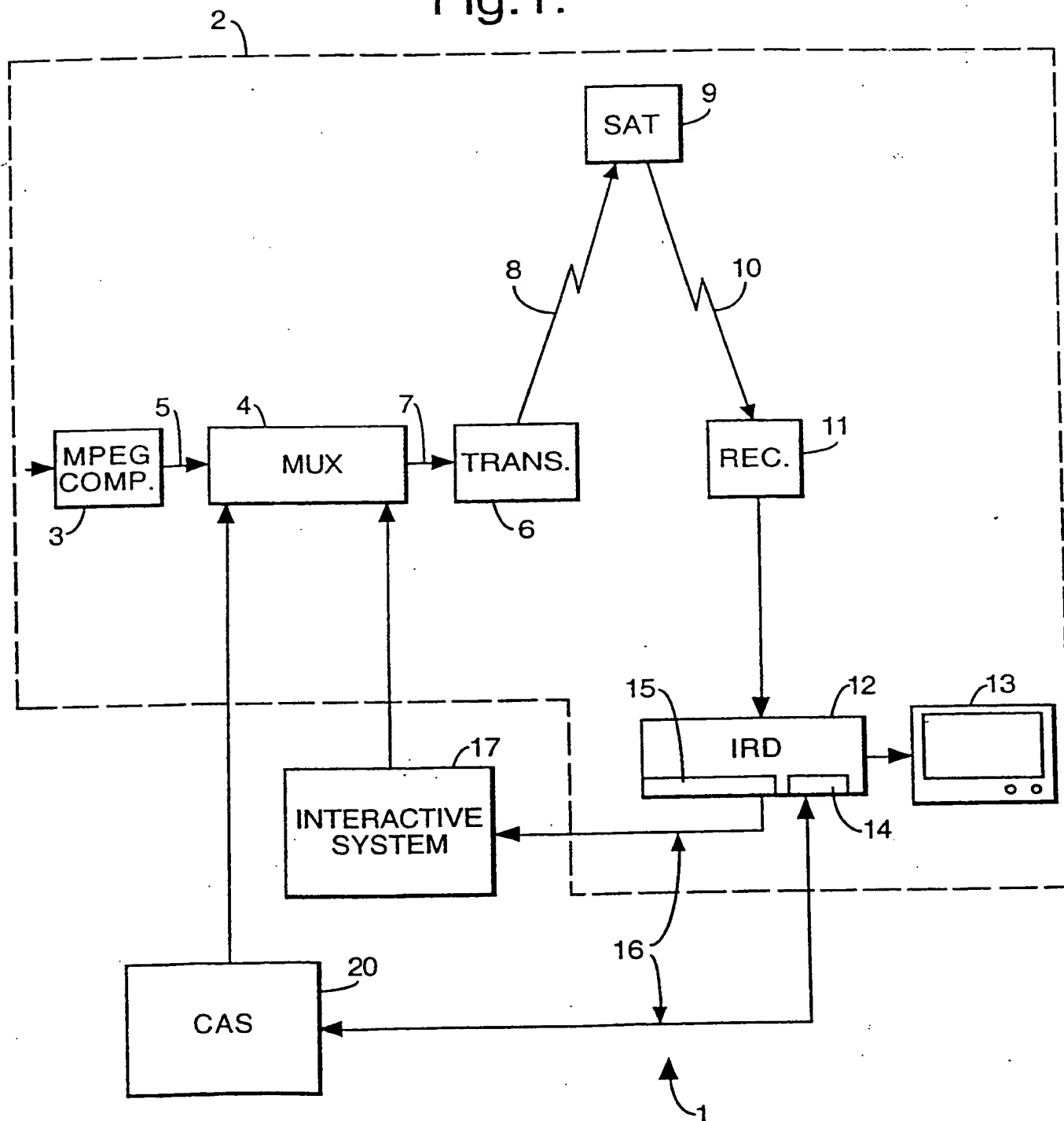


Fig.2.

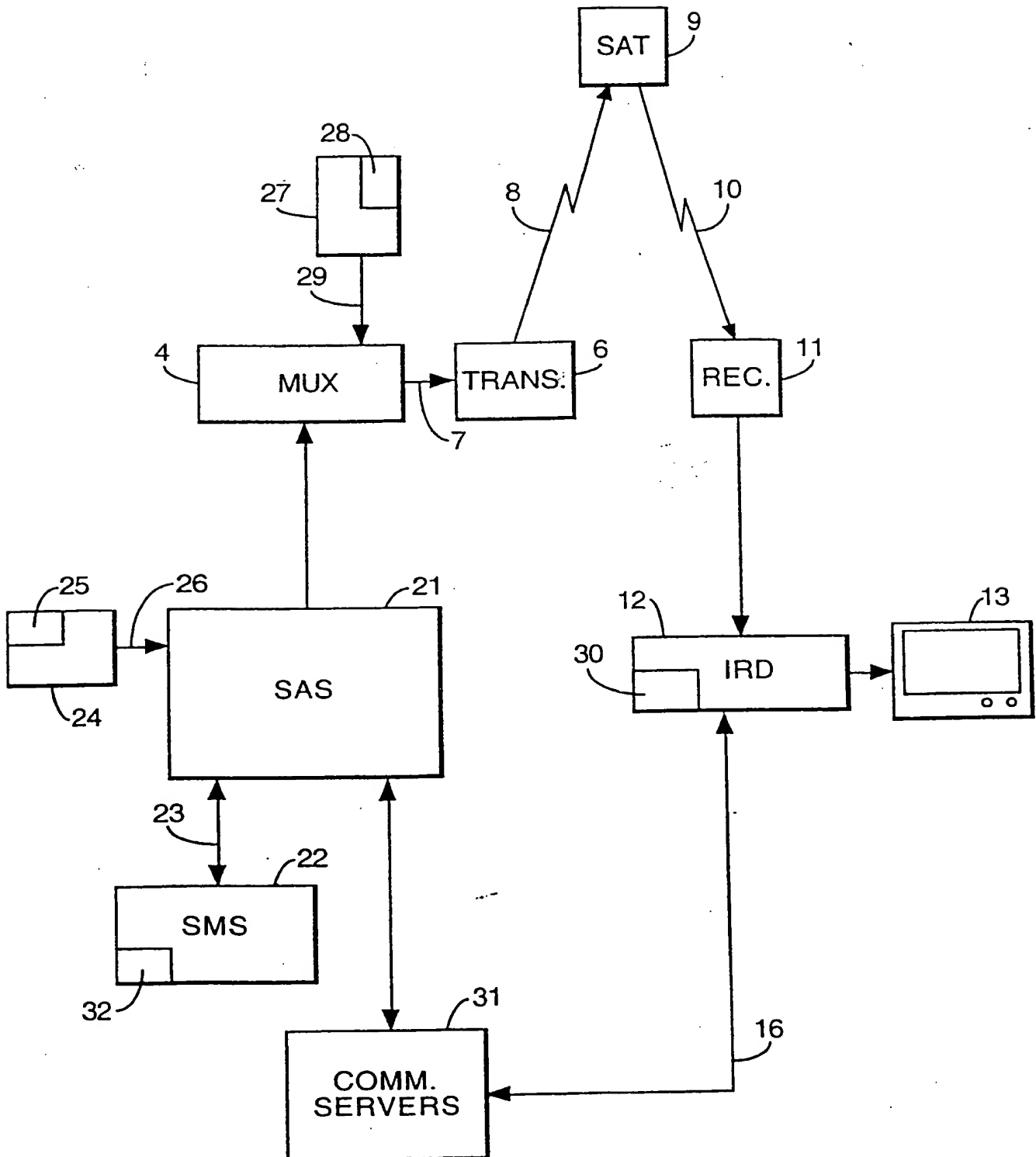


Fig.3.

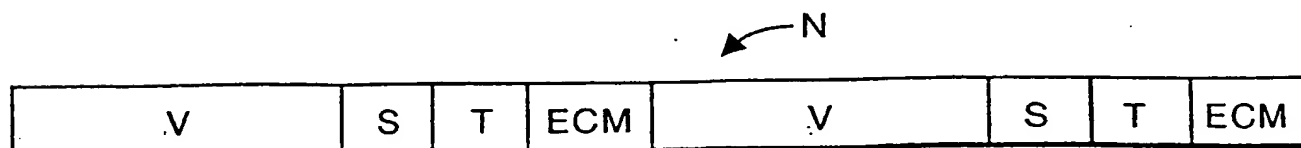
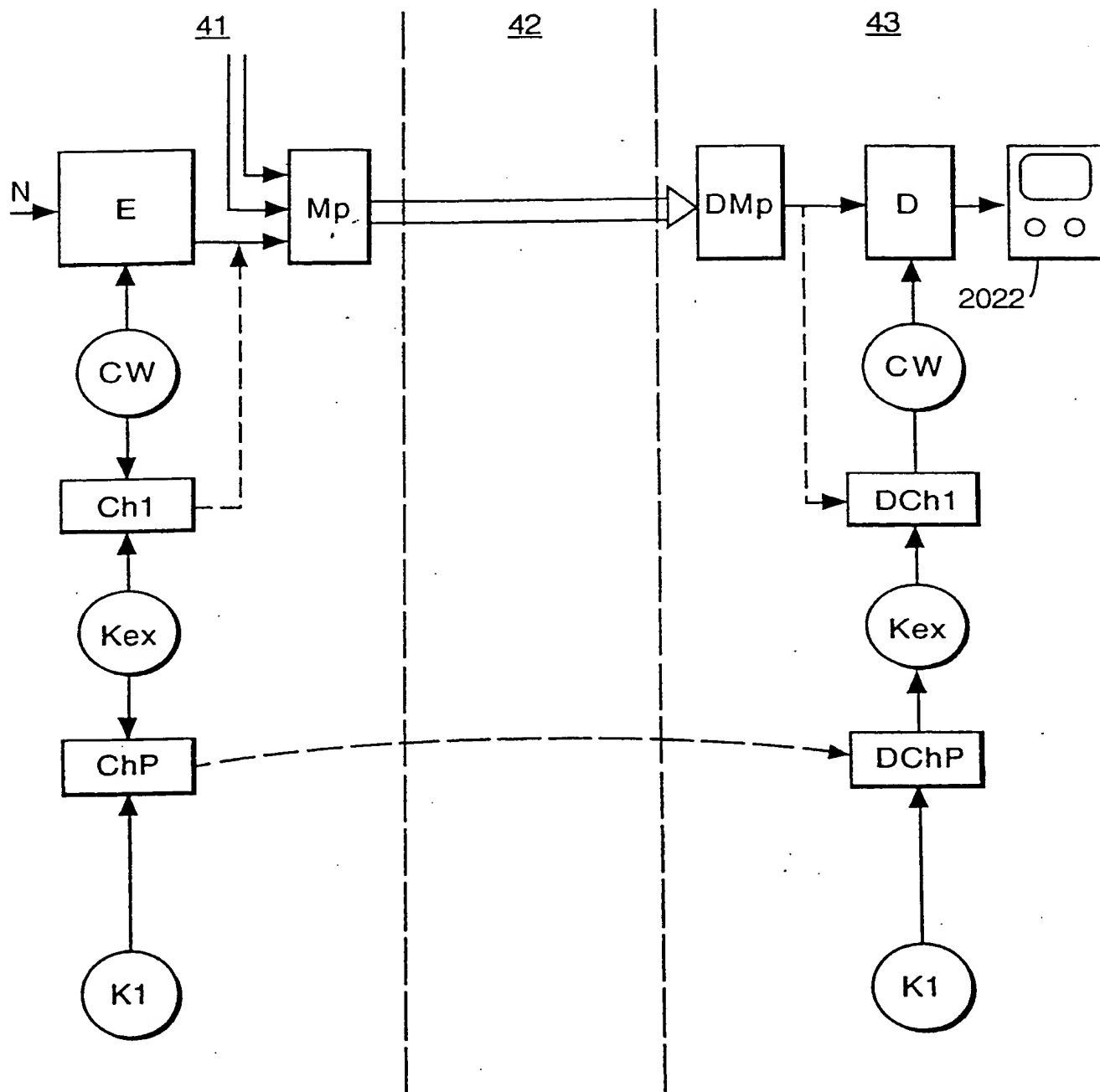
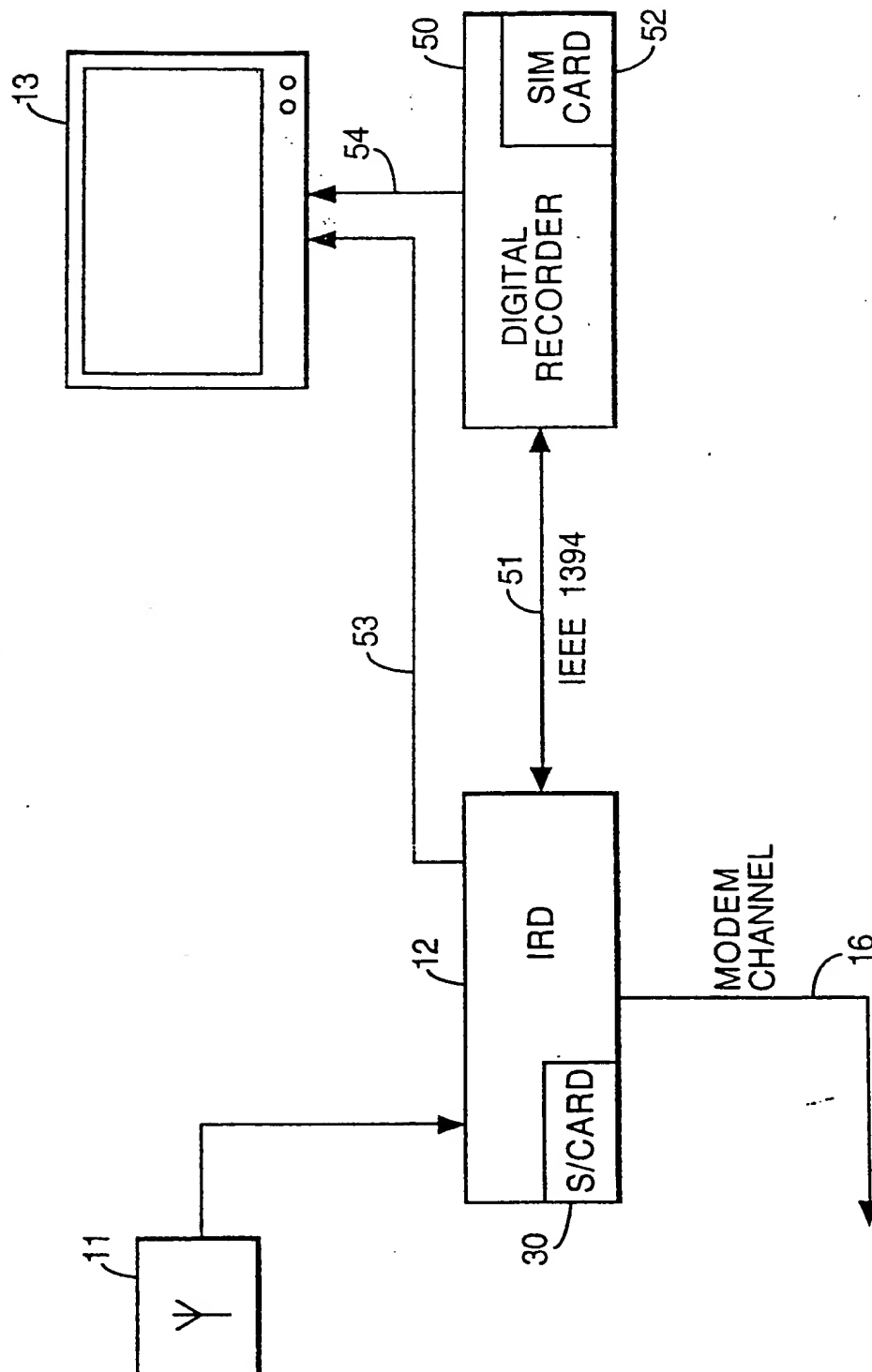


Fig.4.



The diagram illustrates a cryptographic system architecture. It consists of two main components: a **Decoder Card (30)** and a **Recorder Card (52)**.

The **Decoder Card (30)** is divided into three sections:

- CA (55):** Zone System Manager $K_0(NS)$
- Zone Operator (56):** $K'_0(Op1, NS)$, $K'_1(Op1, GN)$, $K'_2(Op1, Z)$
- NS (57):**

The **Recorder Card (52)** is divided into four sections:

- DES (58):**
- CA (60):** Zone System Manager $K_0(NSIM)$, $T(NSIM)$
- Zone Operator (61):**
- NSIM (63):**

Arrows indicate data flow from the Decoder Card (30) to the Recorder Card (52).

58

RECORDER CARD

52

59

60

61

63

DES

CA
Zone System
Manager
KO(NSIM)
T(NSIM)

Zone Operator

NSIM

Fig.6.

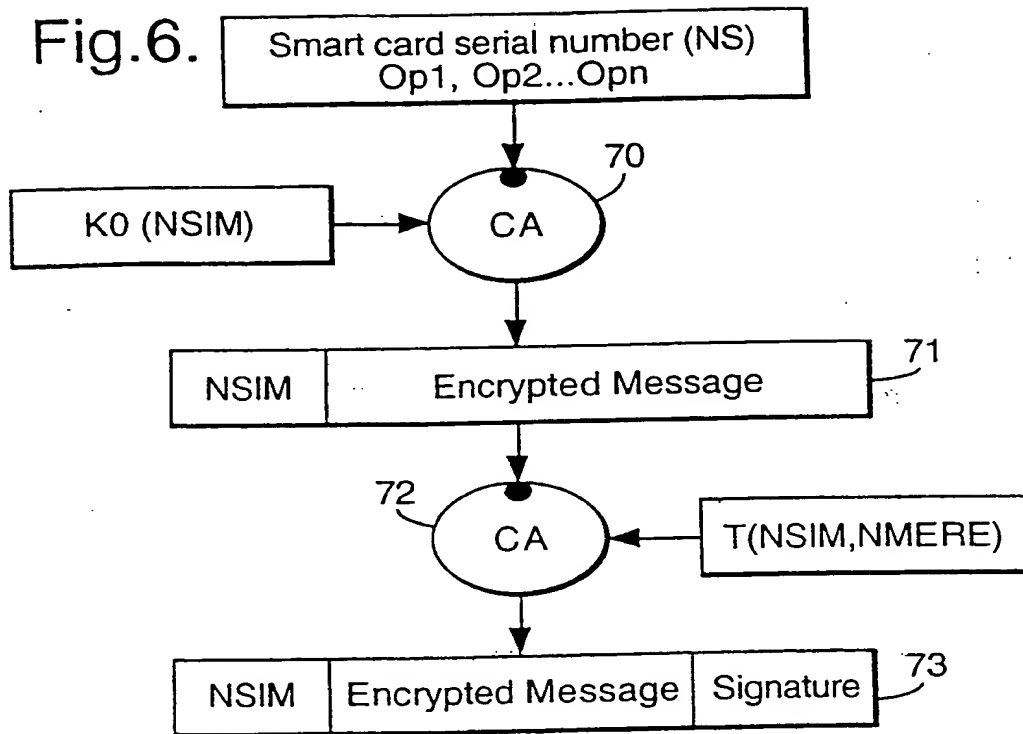


Fig.7.

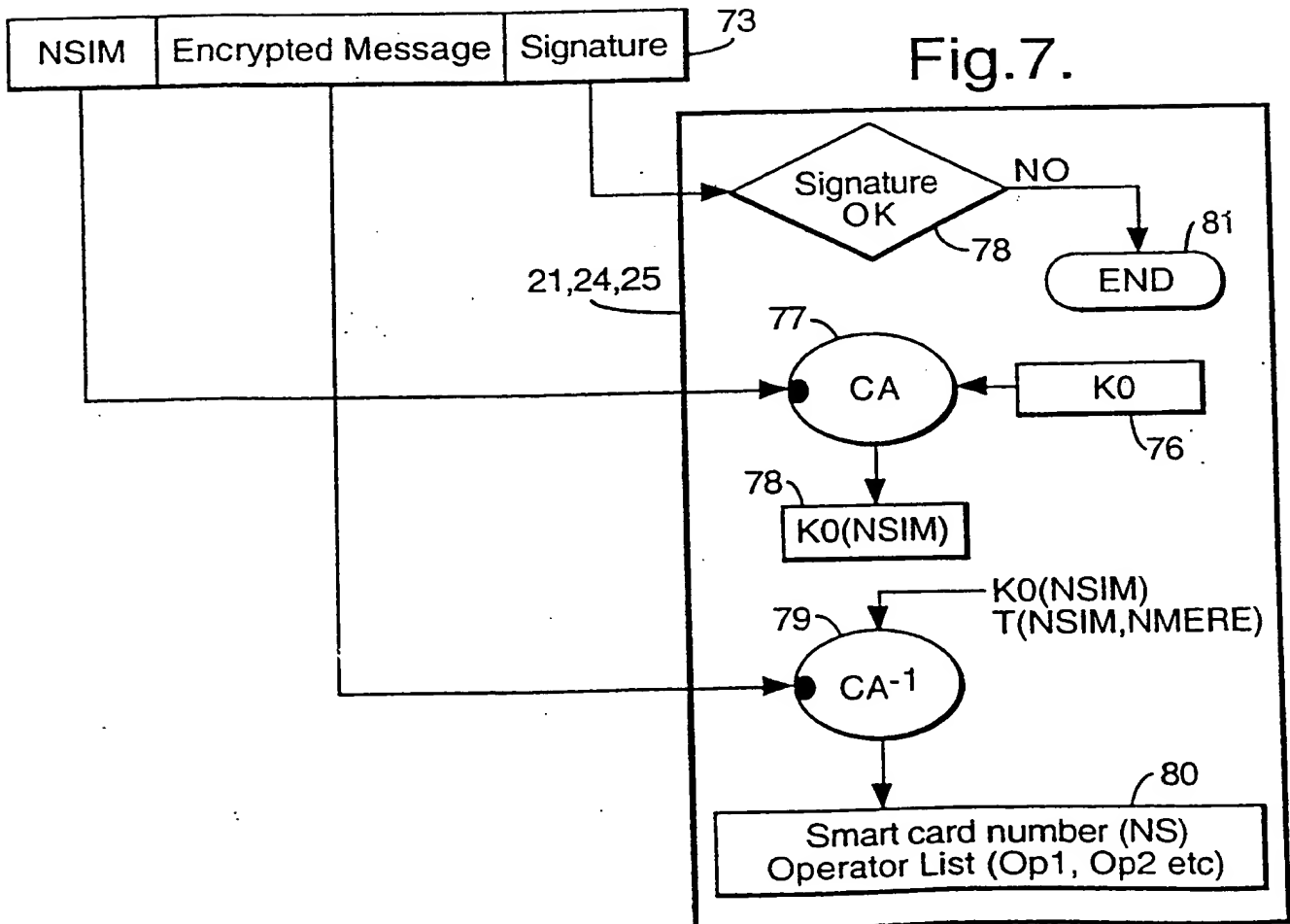


Fig.8.

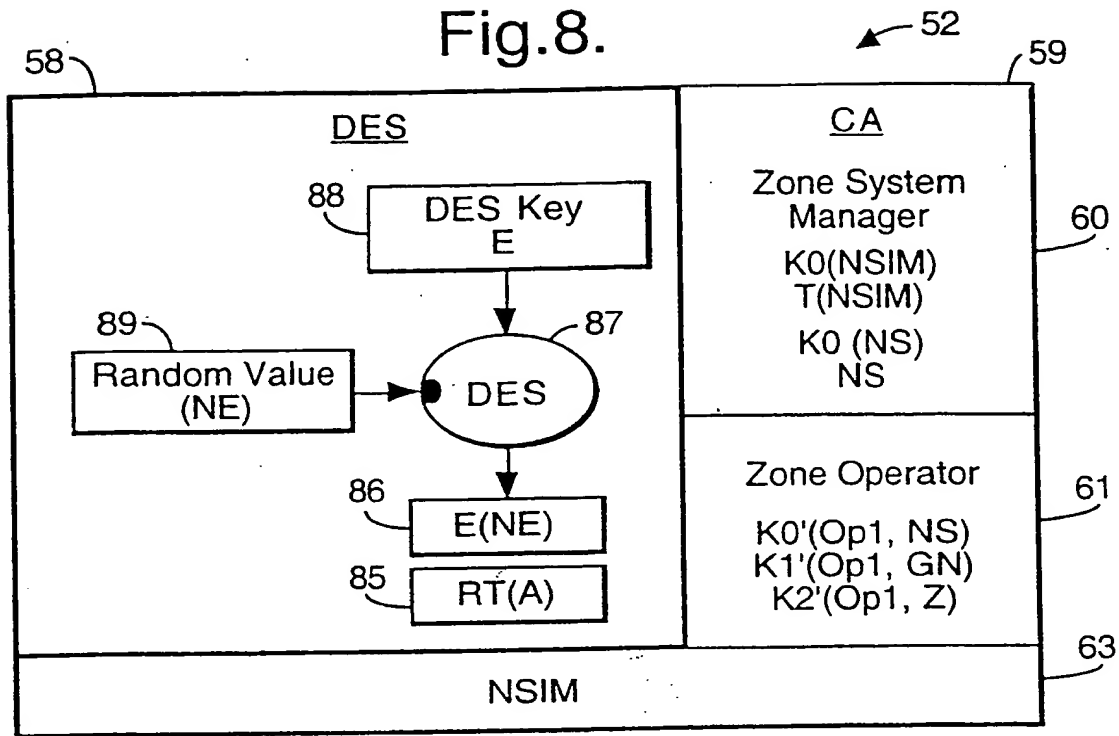


Fig.11.

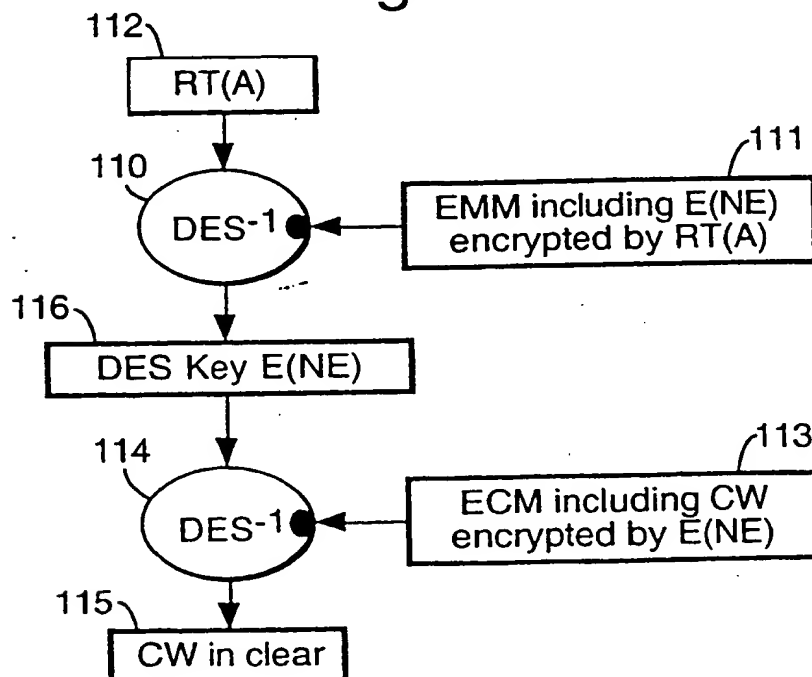


Fig.9.

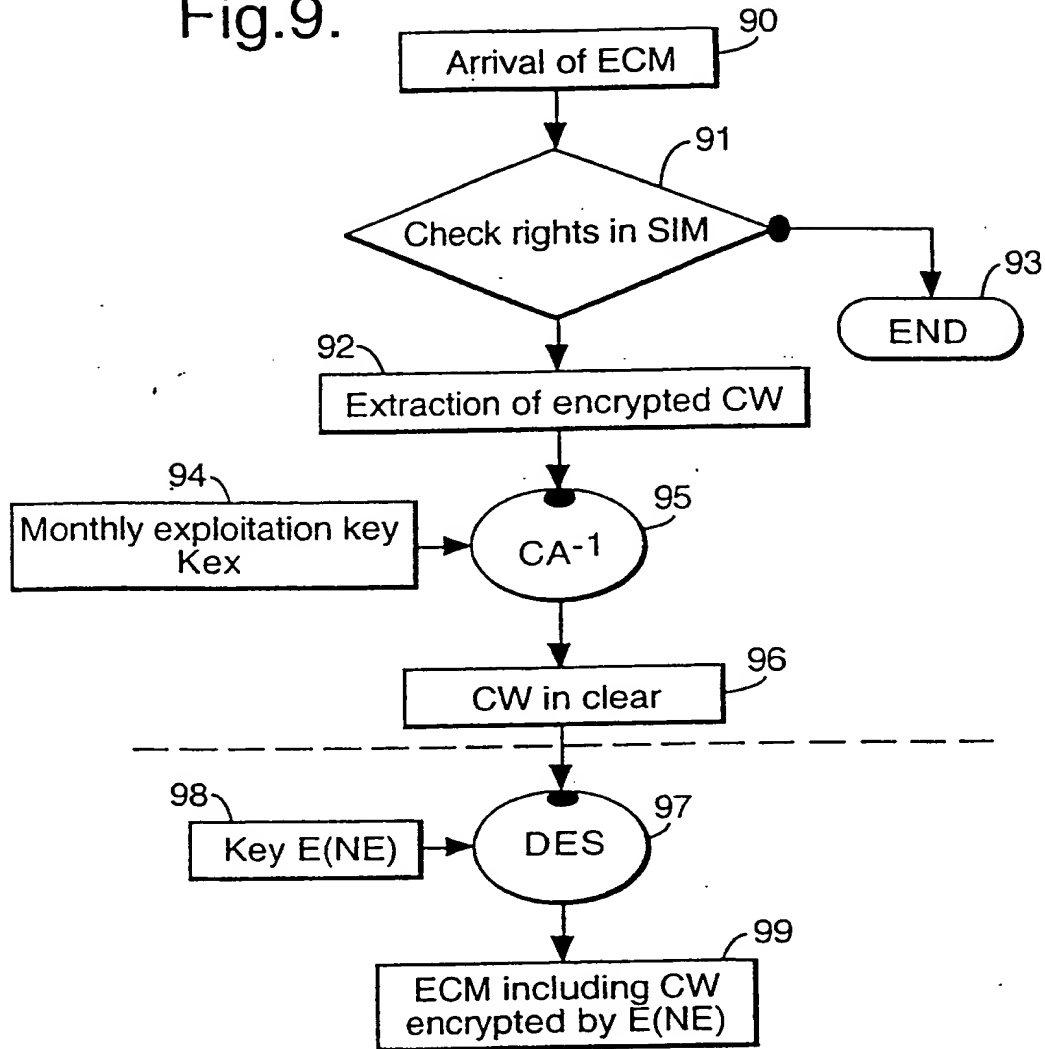


Fig.10.

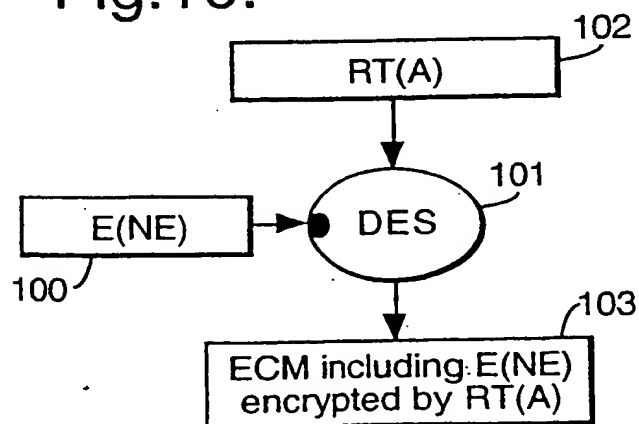
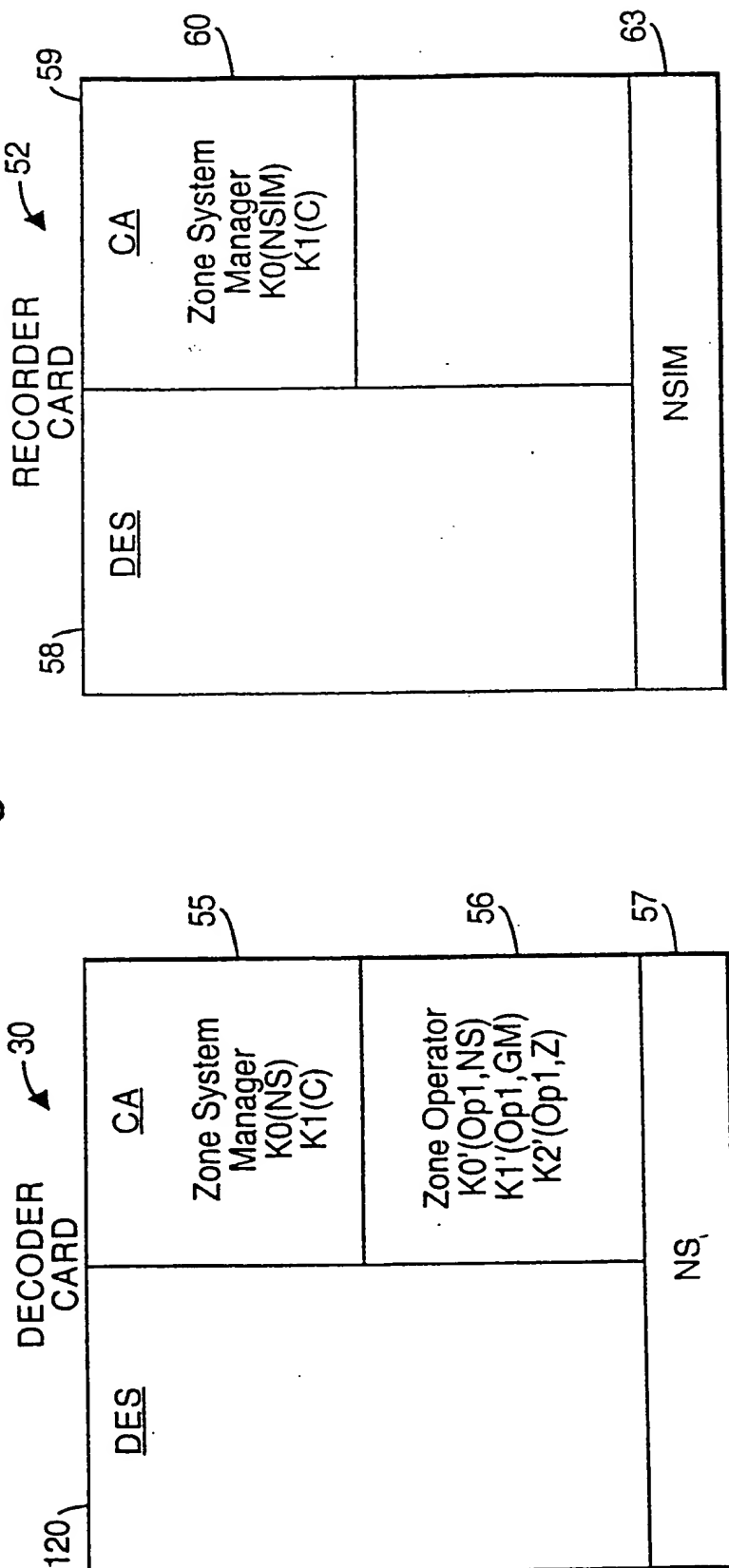
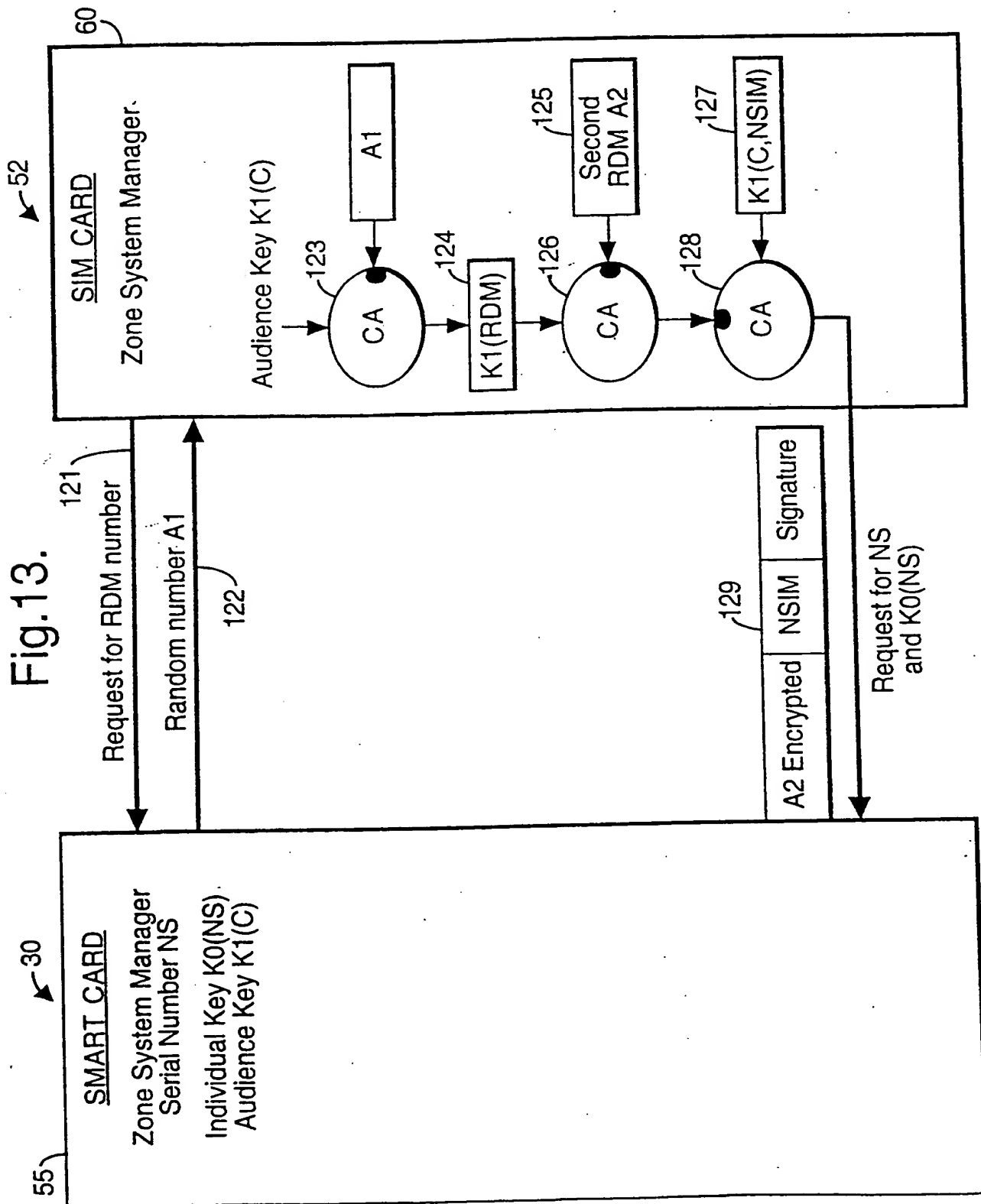


Fig.12.





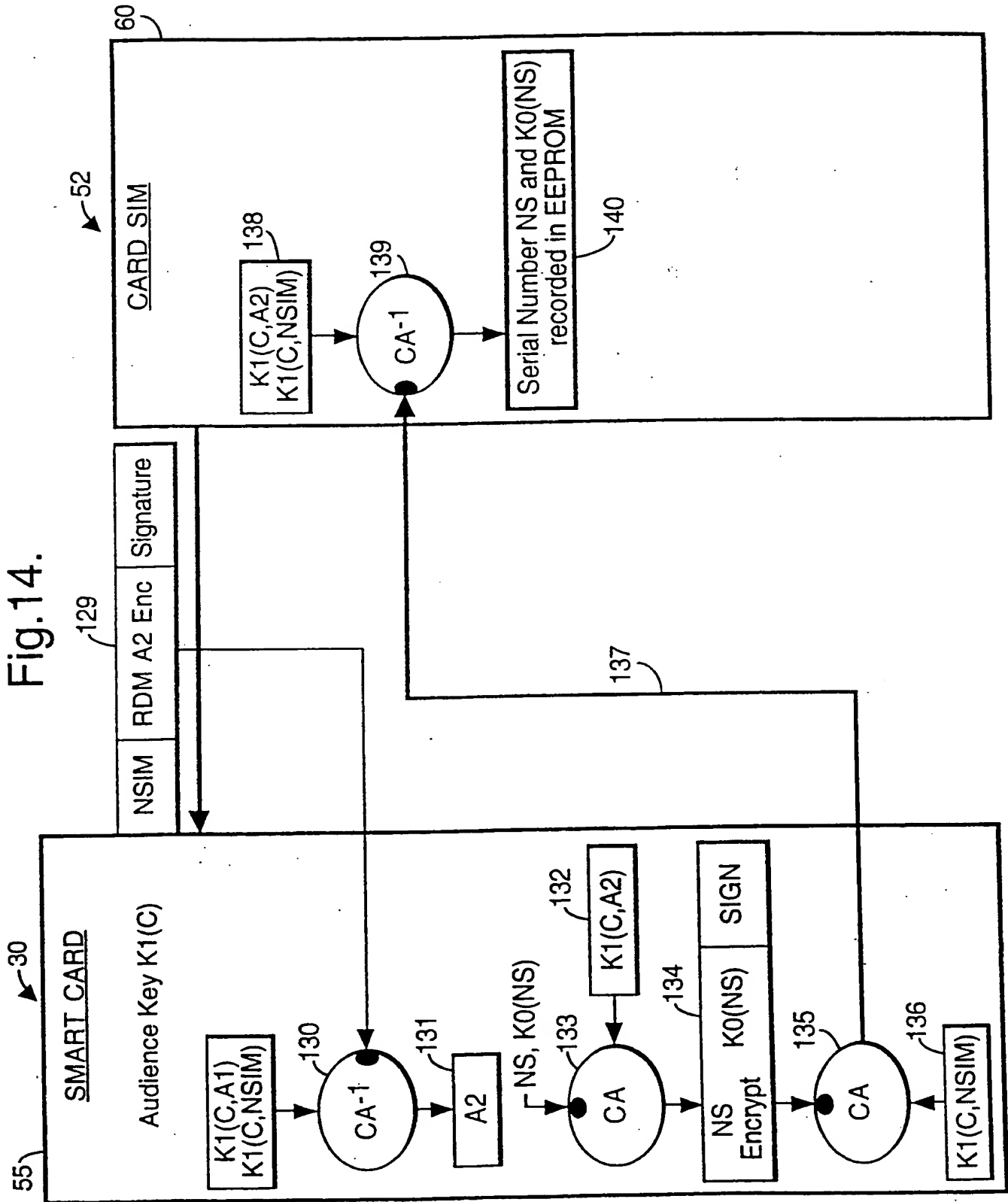


Fig.15.

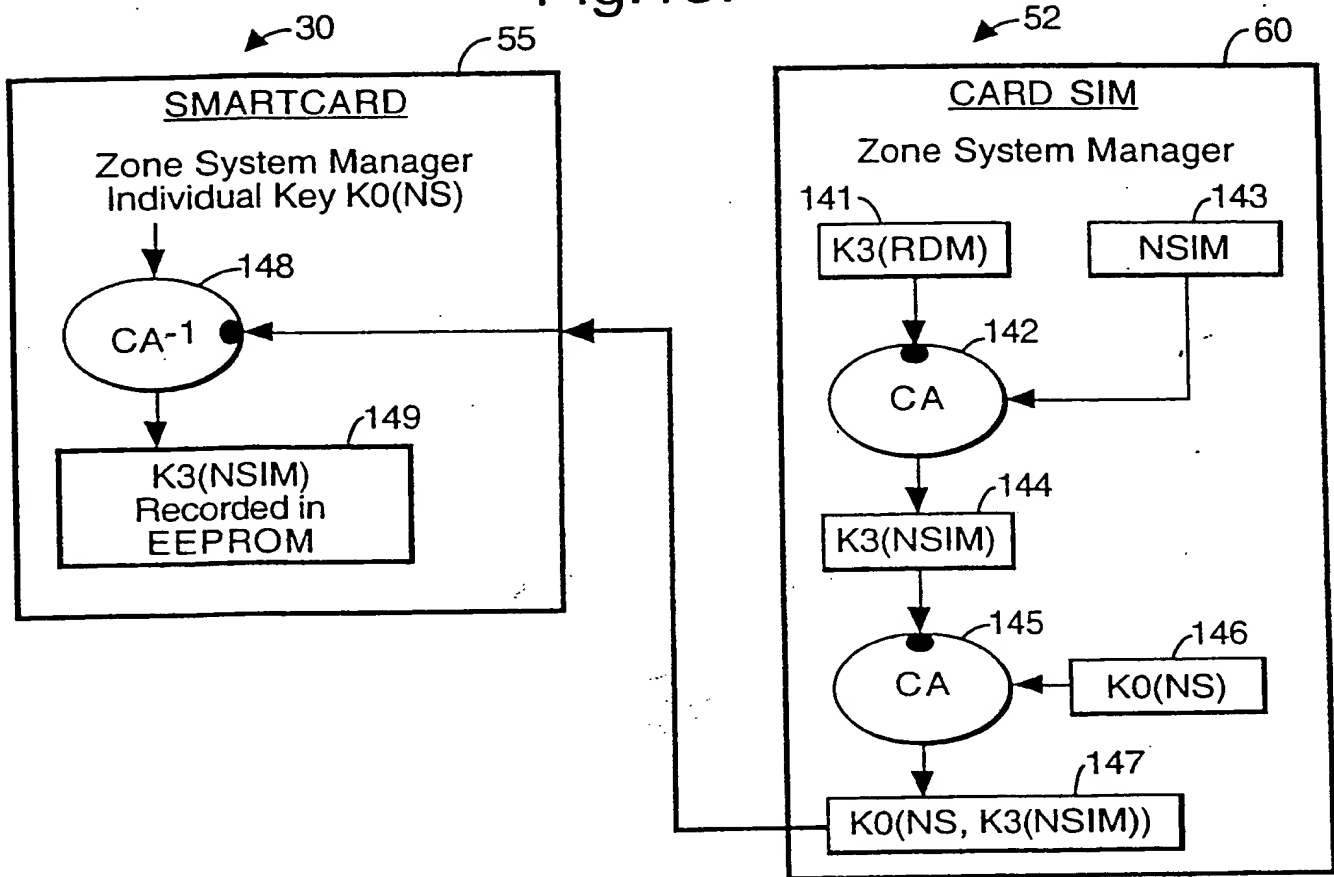


Fig.16.

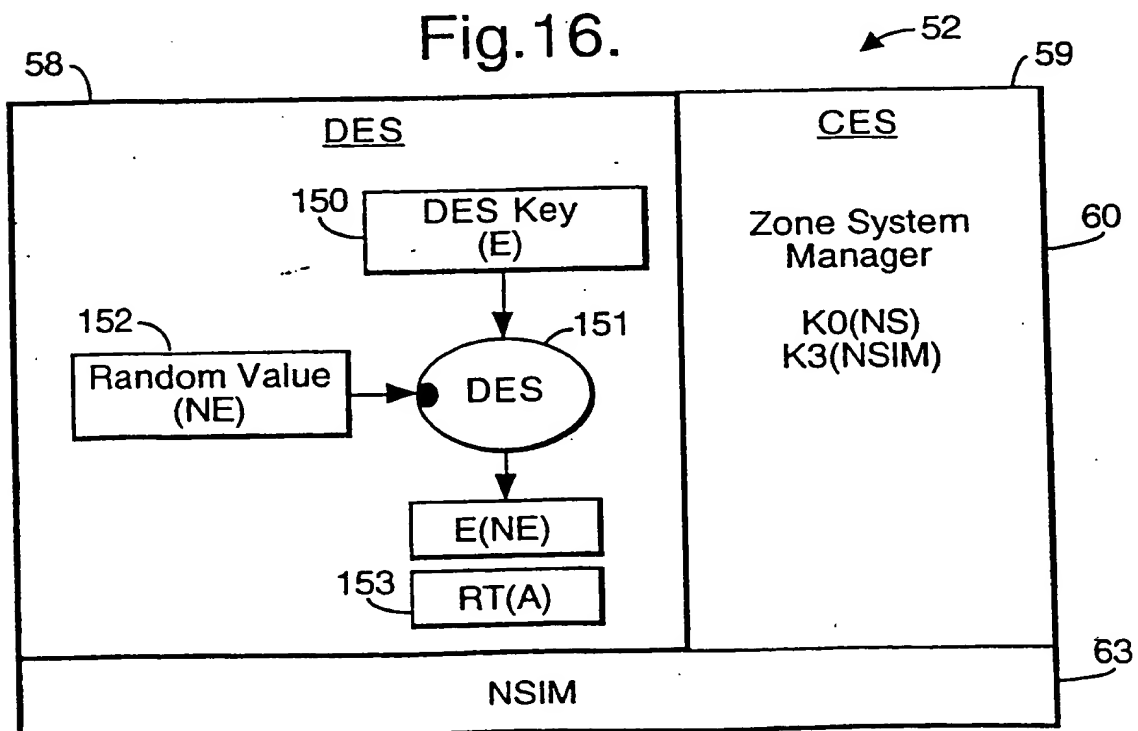


Fig.17.

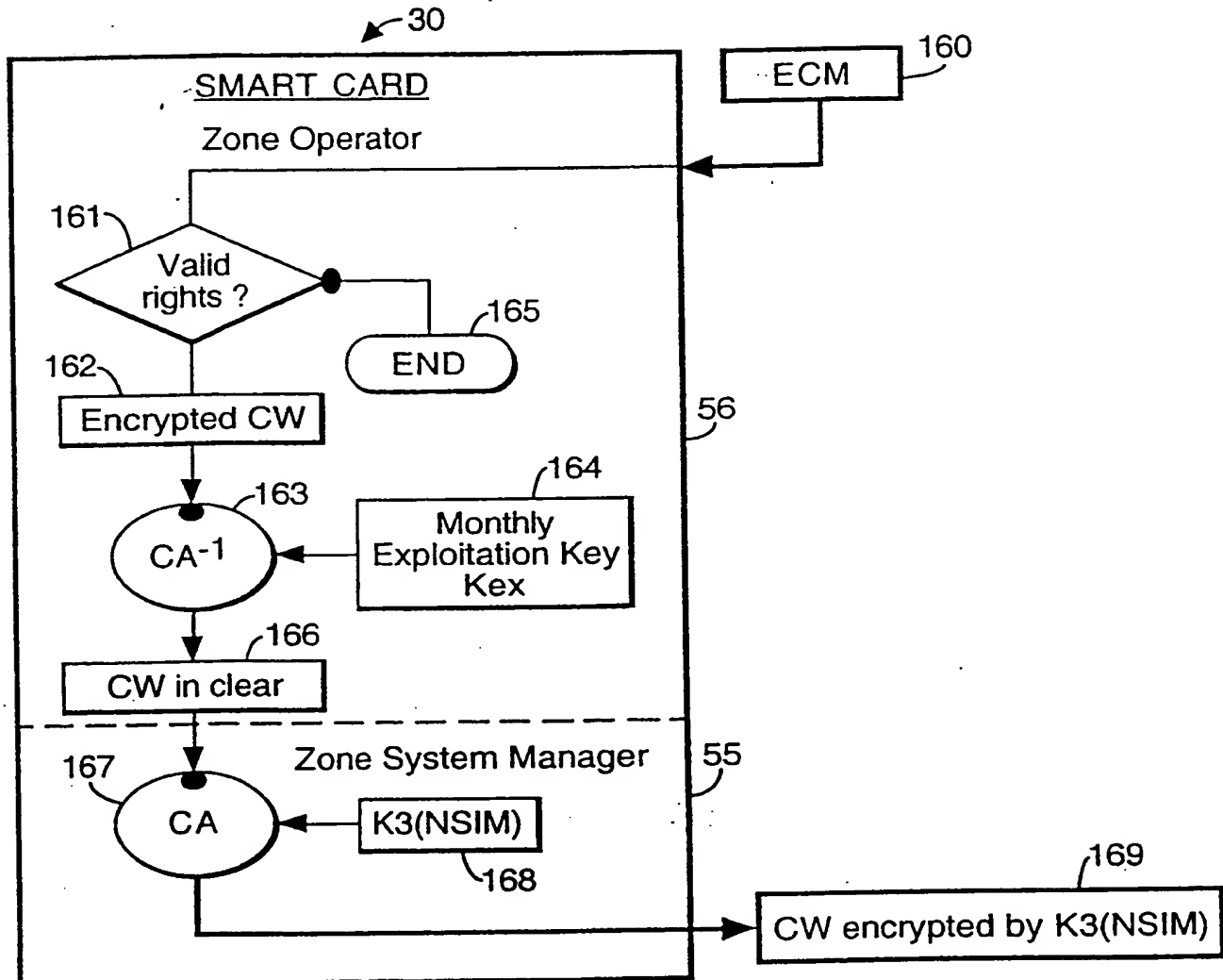


Fig.18.

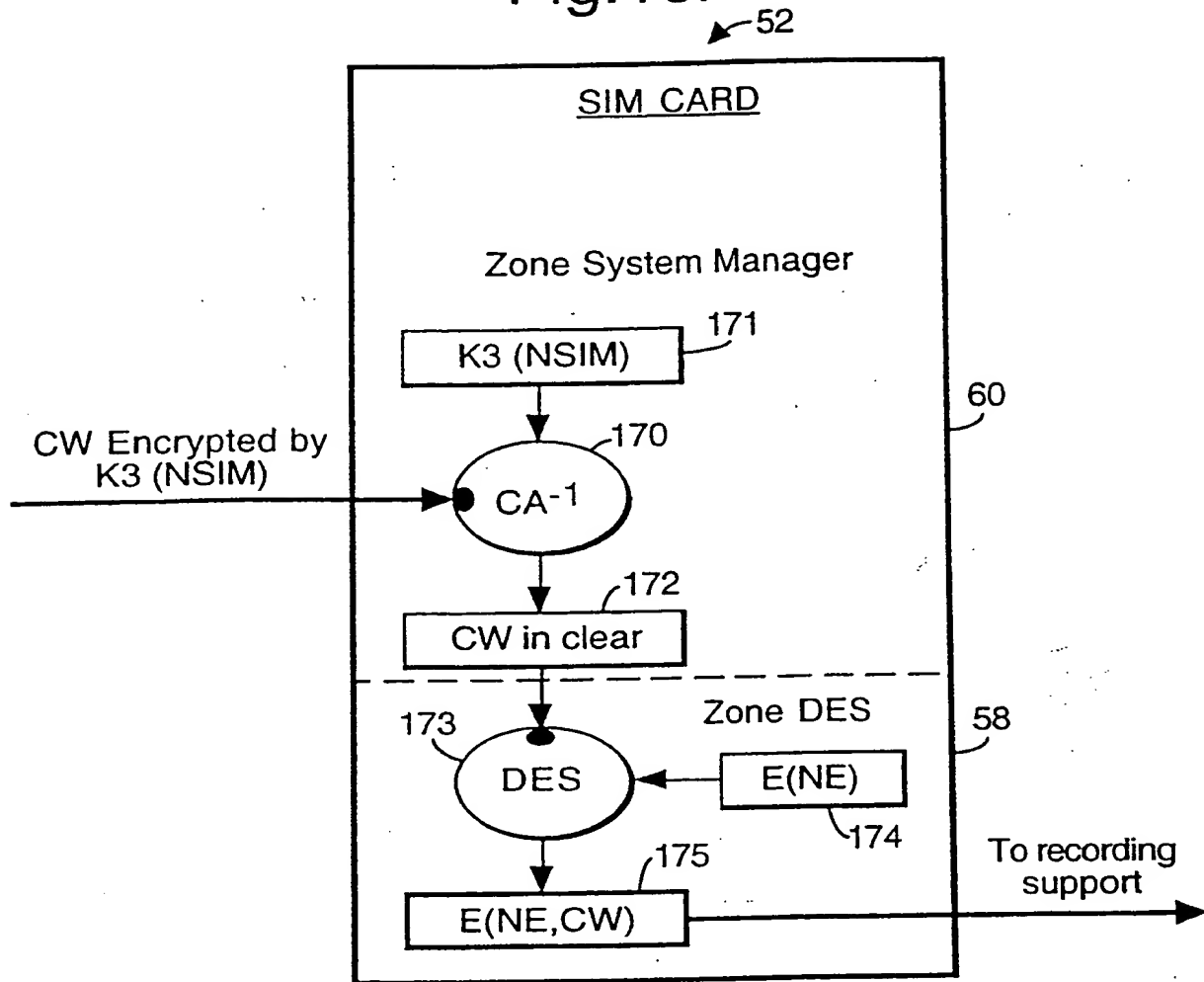


Fig.19.

